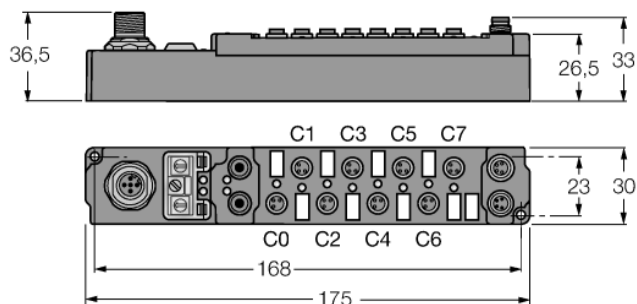


用于DeviceNet的piconet耦合模块

4通道pnp输入滤波3ms

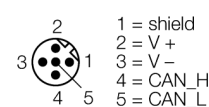
4 Digital Outputs 0.5 A

SDNL-0404D-0003

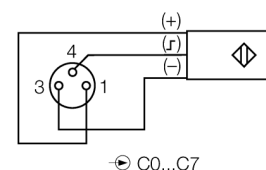


- 配置接口
- 功能参数化
- 通过I/O-ASSISTANT支持
- 直接连接至现场总线
- 直接连接至IP Link
- 玻璃光纤加固外壳
- 模块电路封装
- 金属接插件
- 防护等级IP67

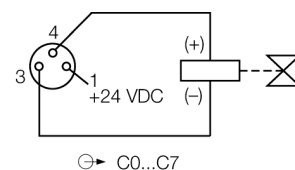
M12 × 1 现场总线



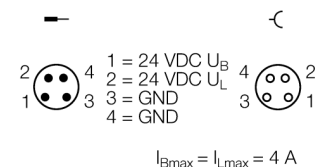
M8 × 1 输入



M8 × 1 输出



M8 × 1 电源



型号	SDNL-0404D-0003
货号	6824227
通道数	8
工作/负载电压	20...29 VDC
工作电流	≤ 60 mA
现场总线传输速率	125/250/500 kbps
现场总线地址设定	0 to 99
服务接口	parameterisation via I/O-ASSISTANT
电气隔离	Fieldbus to operational voltage
Fibre-optic length	≤ 15 m
通道数	4 digital inputs acc. to EN 61131-2
输入电压	20...29 VDC via operating voltage
低电平信号电压	-3...5 VDC (EN 61131-2, type 2)
高电平信号电压	11...30 VDC (EN 61131-2, type 2)
输入延迟	3 ms
最大输入电流	6 mA
通道数	4 digital outputs acc. to EN 61131-2
输出电压	20...29 VDC, 来自负载电压
通道输出电流	0.5 A, 短路保护
负载类型	阻性, 感性, 灯
开关频率	≤ 500 Hz
同步因数	1
尺寸 (长/宽/高)	30 x 175 x 26.5 mm
振动测试	符合EN 60068-2-6标准
冲击测试	根据 DIN EN 60068-2-27
电磁兼容性	符合EN 61000-6-2/EN 61000-6-4标准
防护等级	IP67
认证	CE, cULus

LEDs

	LED designation	Status green	Status red	Function
DeviceNet™	RUN / ERR (DN)	flashes		Start up successful / module is assigned to master
		ON		Module is assigned to master / data exchange with master OK
		OFF		V+, V- (24 VDC) not yet applied / transmission rate not yet detected
			flashes	I/O connection interrupted
			ON	MacID double assignment
			OFF	V+, V- (24 VDC) not yet applied / transmission rate not yet detected
IP-Link / module status	RUN / ERR (I/O)	flickers/ON	OFF	Receiving error-free IP-Link protocols
		flickers	flickers	Receiving faulty IP-Link protocols
		OFF	flickers	Receiving faulty IP-Link protocols / system fault
		OFF	ON	No receipt of IP-Link protocols / module error
Inputs	0...3	OFF		Input inactive (not dampened)
		ON		Input active (dampened)
Outputs	4...7	OFF		Output inactive (not switched)
		ON		Output active (switched)
Power supply	U _B	OFF		Operating voltage U _B < 18 VDC
		ON		Operating voltage U _B ≥ 18 VDC
	U _L	OFF		Load voltage U _L < 18 VDC
		ON		Load voltage U _L ≥ 18 VDC

过程映像中的数据

		Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
4 bit input data and output data each are mapped.	Input	Is used by the physically following bit-oriented extension module connected via the IP Link.				C1P2	C1P4	C0P2	C0P4
	Output					C3P2	C3P4	C2P2	C2P4

C... = Connector no., P... = Pin no.